1/9

FIG.1A

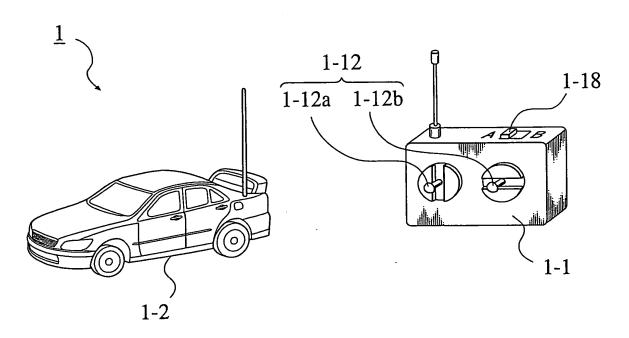
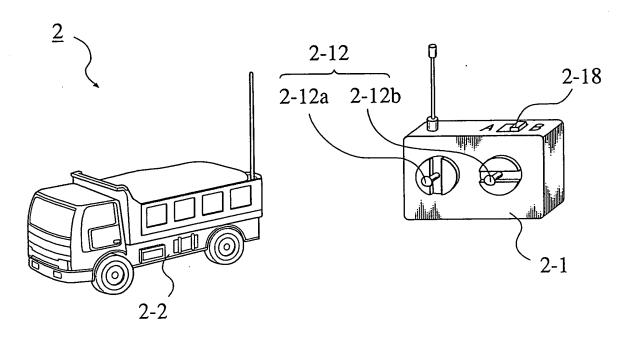
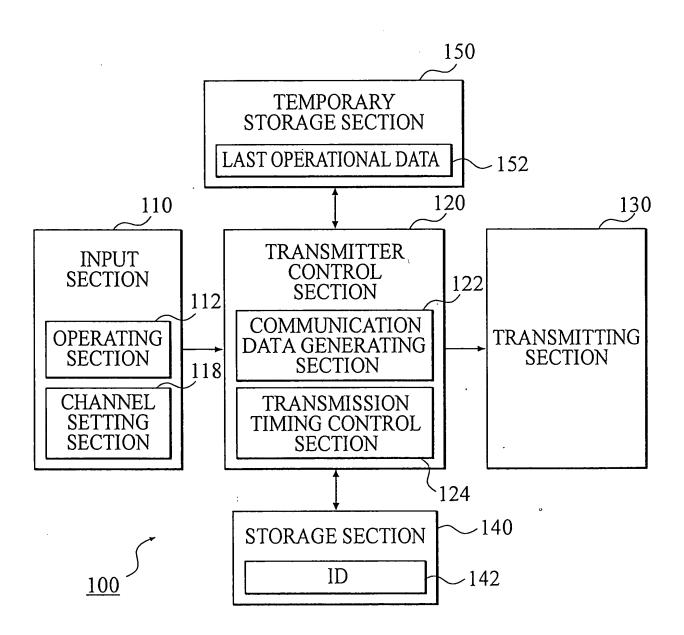


FIG.1B



2/9

FIG2



3/9

FIG3

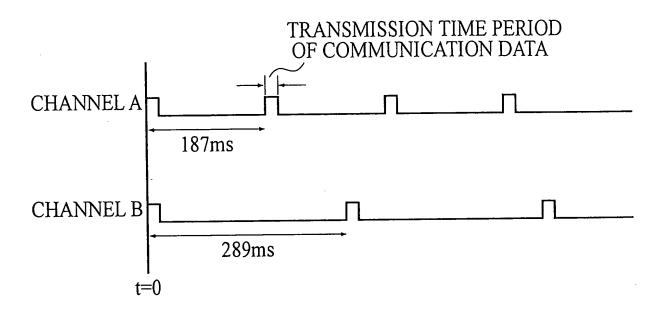


FIG4

OPERATIONAL DATA

SYNCHRONIZATION BIT	ID	RUNNING INSTRUCTION BIT	STEERING INSTRUCTION BIT	INSPECTION BIT
4 BITS	2 BITS	4 BITS	4 BITS	4 BITS

4/9

FIG.5A

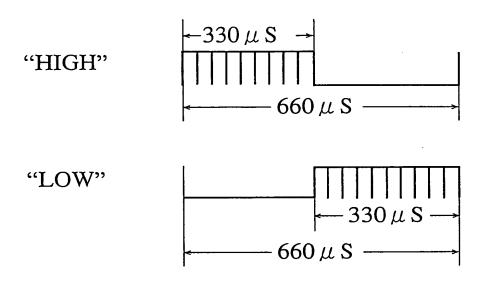
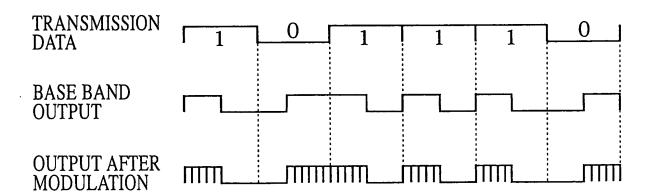
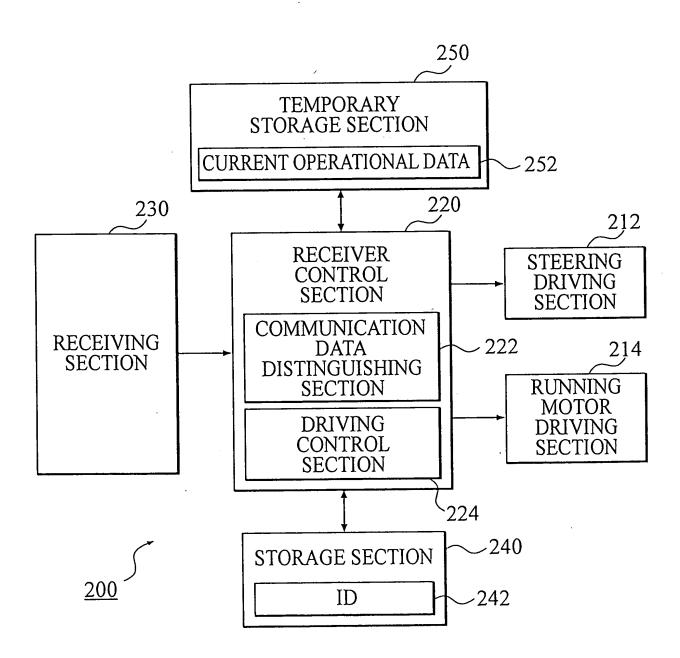


FIG.5B



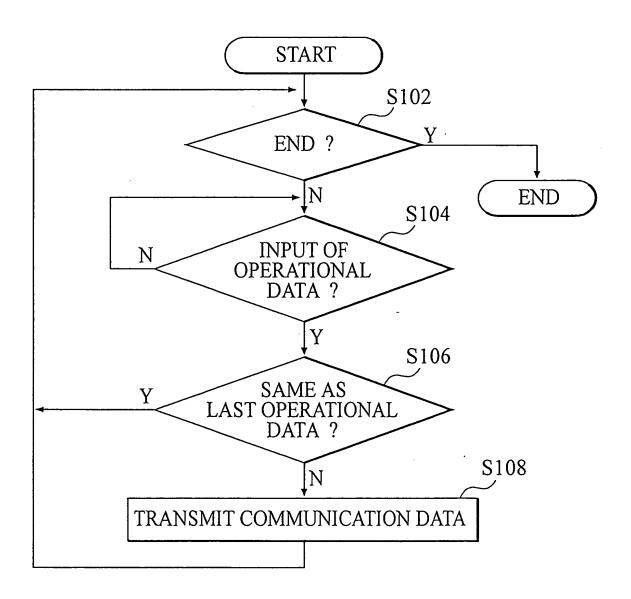
5/9

FIG6



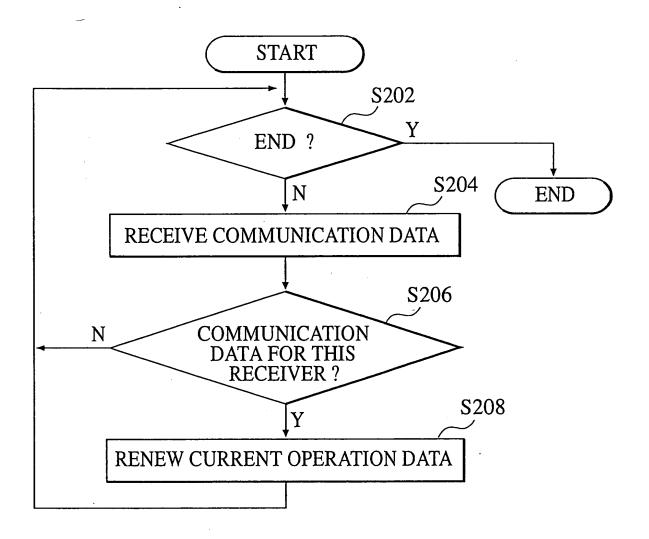
6/9

FIG7

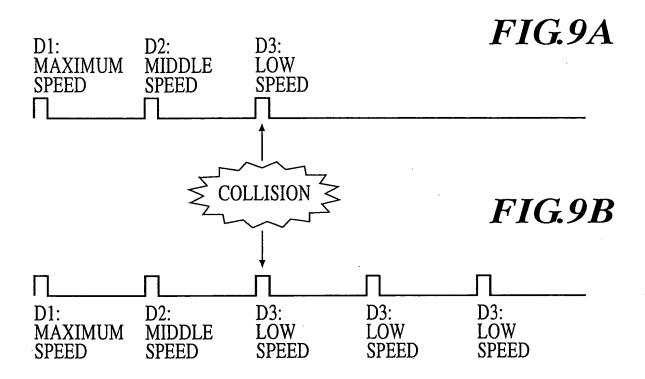


7/9

FIG.8



8/9



9/9 **FIG10**

